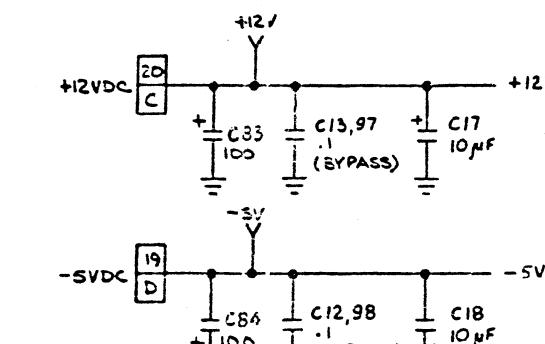
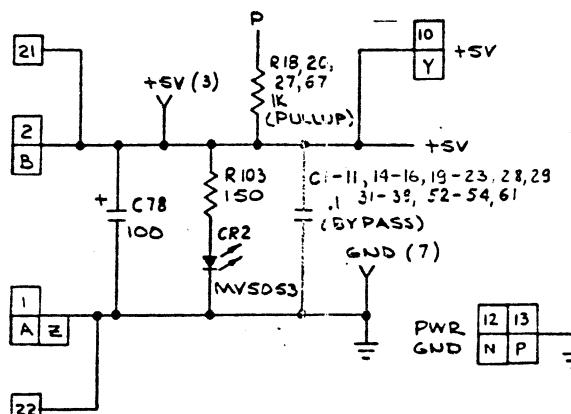
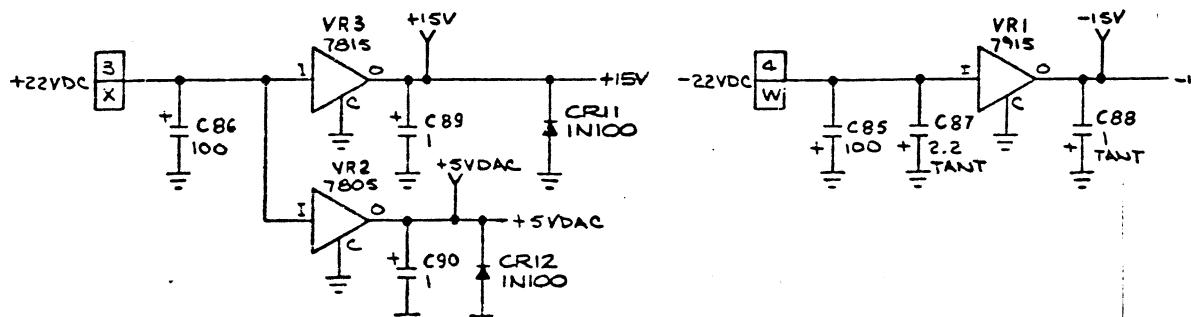


IC CHAR

TYPE	VOLTAGE / PIN NO.							UNUSED GATES
	GND	+5V	+5V DAC	+12V	+15V	-5V	-5V	
74S03	7	14						
74LS00	7	14						
74S02	7	14						
74LS02	7	14						
74S04	7	14						P5, P5, P5,
74LS04	7	14						
74LS06	7	14						
74LS08	7	14						
74LS12	7	14						K4
74LS11	7	14						
74LS14	7	14						
74LS22	7	14						M5
74LS27	7	14						
74LS32	7	14						L2
74LS42	8	16						
74S74	7	14						
74LS74	7	14						
74S86	7	14						B7
74LS86	7	14						
74LS129	8	16						
74LS139	8	16						
74LS157	8	16						
74LS161	8	16						
74LS164	7	14						
74LS174	8	16						
74LS175	8	16						
74LS191	8	16						
74193	8	16						
74LS193	8	16						
74LS194	8	16						
74LS244	10	20						
74LS245	10	20						
74S260	7	14						
74LS273	10	20						
74LS367	8	16						
74LS393	7	14						
74LS670	8	16						
2114 (RAM)	9	18						
8316E (ROM)	12	21,24		19				
2708 (ROM)	12	24						21
825129 (PROM)	8	16						
6502A (MICROPROCESSOR)	1,21	8						
AM6012	13		20			17		
MC3410	2		14			1		
DAC-08			13			3		
LM319	3,2				11	6		
TLO82					8	4		
TLO84					4	11		
LF13201	5				13	4		
14050	8	1						
7404	7	14						HII, HII, HII, HII
74LS399	8	16						



MEMORY MAP

ADDRESS	DATA	FUNCTION
0000-3FF	X000319XAAAAAAASAAAAAAKID	PROGRAM RAM (4K)
8000-3FF	X0000100X1X1X1X1X1X1X1X	RIGHT COIN
	X1X1X1X1X1X1X1X1X1X1X1X	CENTER COIN
	X1X1X1X1X1X1X1X1X1X1X1X	LEFT COIN
	X1X1X1X1X1X1X1X1X1X1X1X	SLAM SW
	X1X1X1X1X1X1X1X1X1X1X1X	SELF TEST SW
	X1X1X1X1X1X1X1X1X1X1X1X	DIR STEP SW
	X1X1X1X1X1X1X1X1X1X1X1X	HALT
	X1X1X1X1X1X1X1X1X1X1X1X	3KHE
8A32	X100001011X1X1X1X1X1X1X	OPTIONS 0
8C00	X00005101X1X1X1X1X1X1X	OPTIONS 1
10000	X10011000X1X1X1X1X1X1X	COIN COUNTER RIGHT
	X1X1X1X1X1X1X1X1X1X1X1X	COIN COUNTER CENTER
	X1X1X1X1X1X1X1X1X1X1X1X	COIN COUNTER LEFT
	X1X1X1X1X1X1X1X1X1X1X1X	VIDEO INVERT X
	X1X1X1X1X1X1X1X1X1X1X1X	VIDEO INVERT Y
12800	X2222221X1X1X1X1X1X1X1X	VECTOR GENERATOR GO
14000	X0222221X1X1X1X1X1X1X1X	WATCHDOG CLEAR
14000	X100000000000000000000000	VECTOR GENERATOR RESET
16000-27F	X111111X1X1X1A1A1A1A1A	I/O FOR AYX BD
20000-3FF	X01120A1A1A1A1A1A1A1A	VECTOR RAM (2K)
28000-3FF	X01121A1A1A1A1A1A1A1A	VECTOR RAM/VECTOR ROM (2K)
30000-3FF	X011A1A1A1A1A1A1A1A1A	VECTOR ROM (4K)
34000-3FF	X11A1A1A1A1A1A1A1A1A1	PROGRAM ROM (4K)
40000-3FF	X11A1A1A1A1A1A1A1A1A1	PROGRAM ROM (8K)

NOTES:

NOTE: 1. DENOTES J20 CONNECTOR
2. " TEST "
3. " J19 "

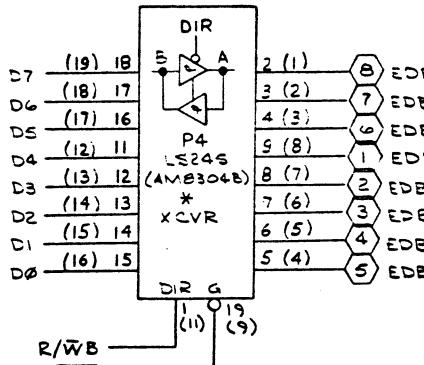
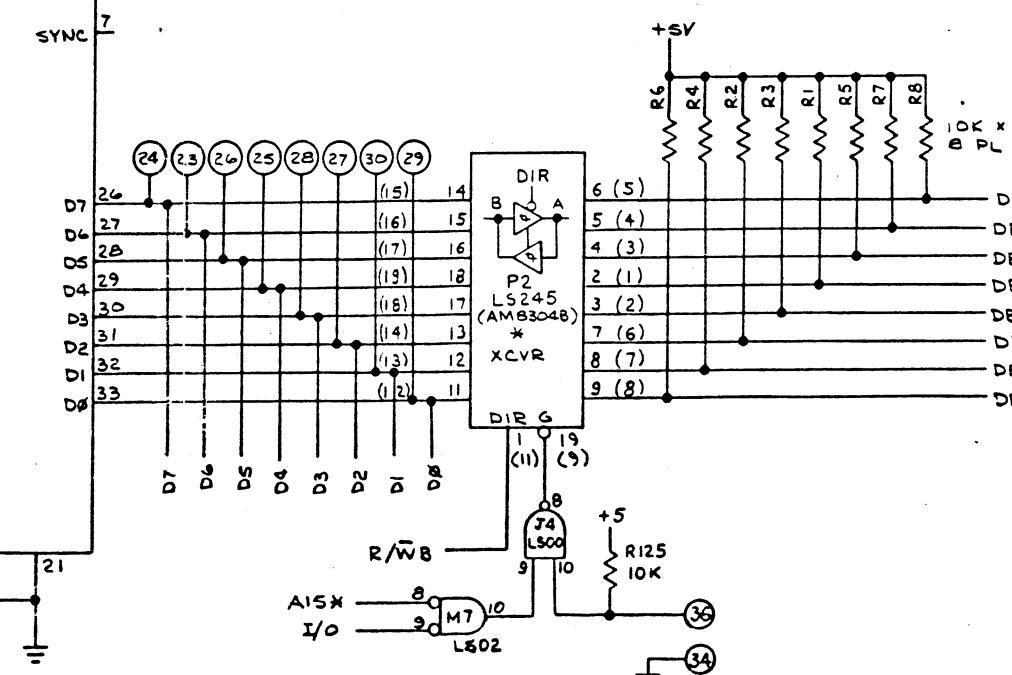
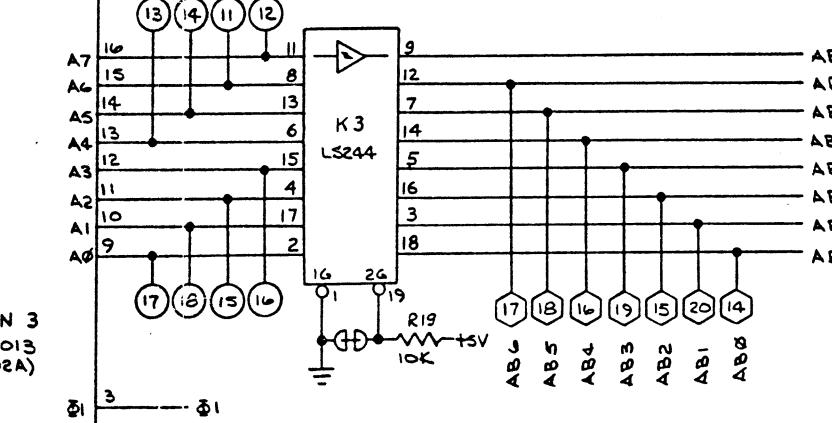
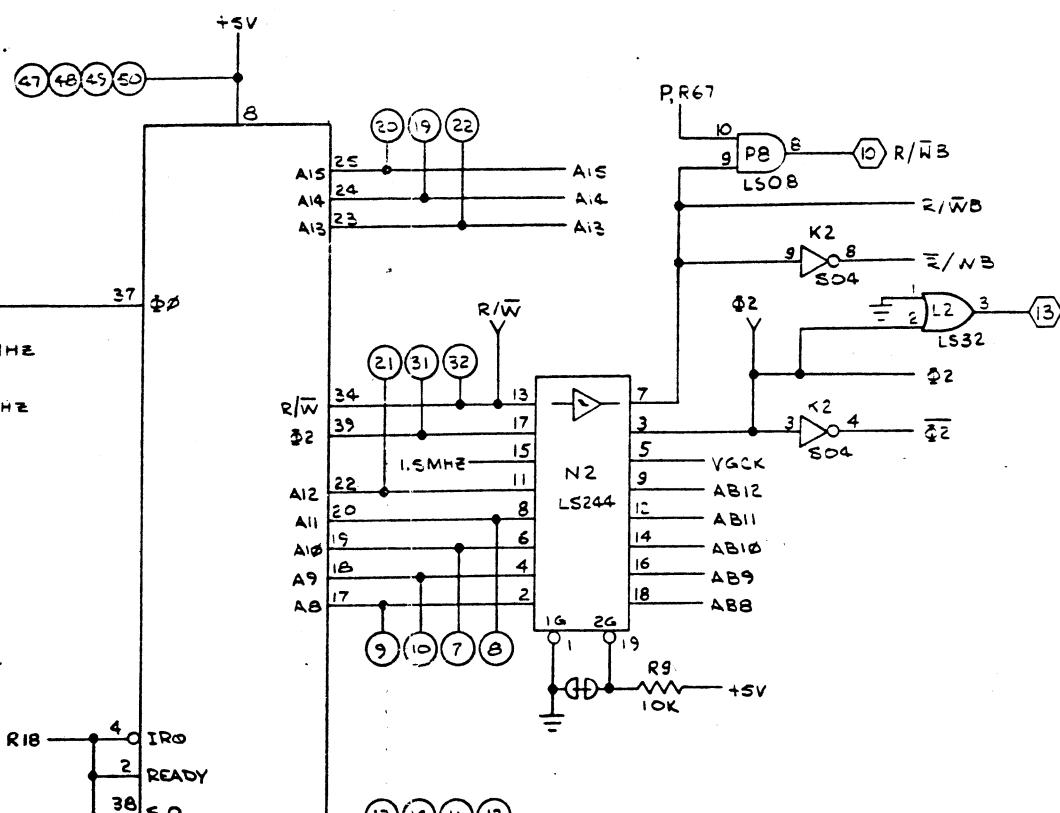
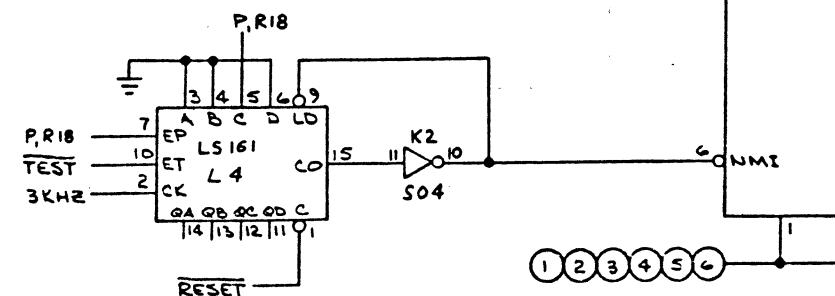
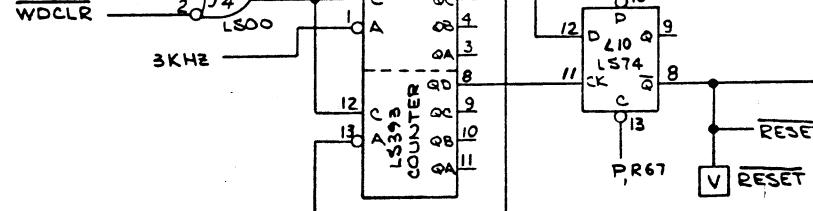
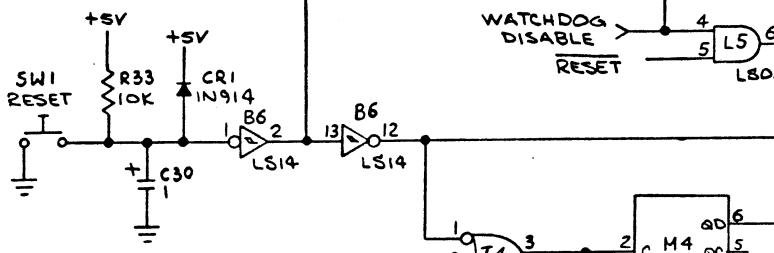
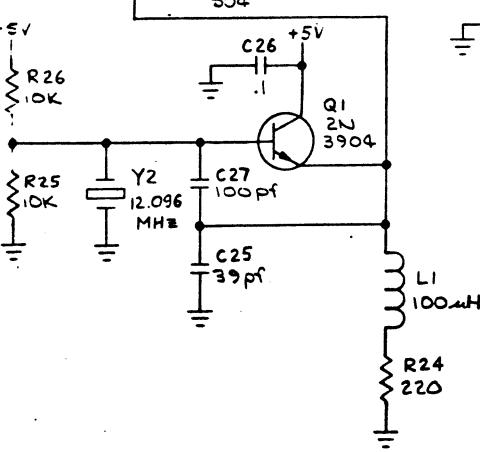
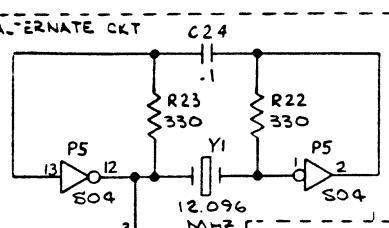
HISTORY PRINT

-01 = MC3410 DAC
-02 = AM6012 ADC

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		SURFACE FINISH	111-11	ENGR. ELECT	DATE	
		MATERIAL:		INDUSTRIAL DESIGN	DATE	
		FINISH:		HARNESS DESIGN	DATE	
		PROJECT ENGR		7-5-83	DATE 11-1-83	
		MFG ENGR		7-5-83	DATE 11-1-83	
		SCALE		DRAWING NO D 035742-XX		
		SHEET 1 OF 7		REV B		

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NOTE:
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INSTEAD OF 74LS245, 2 PLACES
THIS PAGE.

PRINT

SYNC, RESET, MICROPROCESSOR

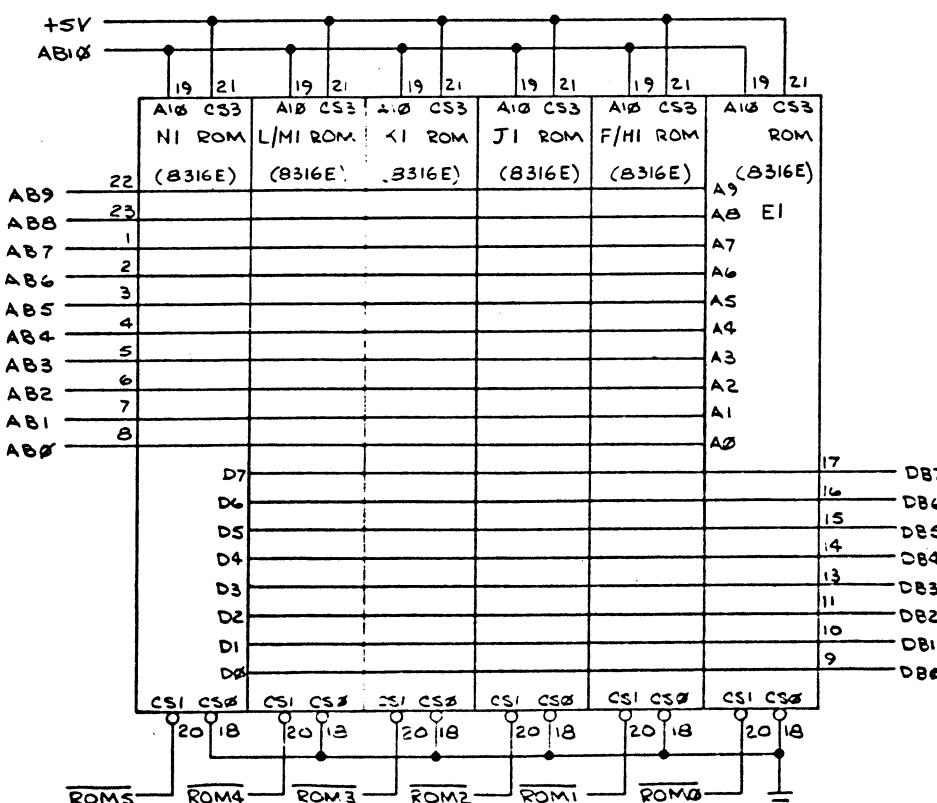
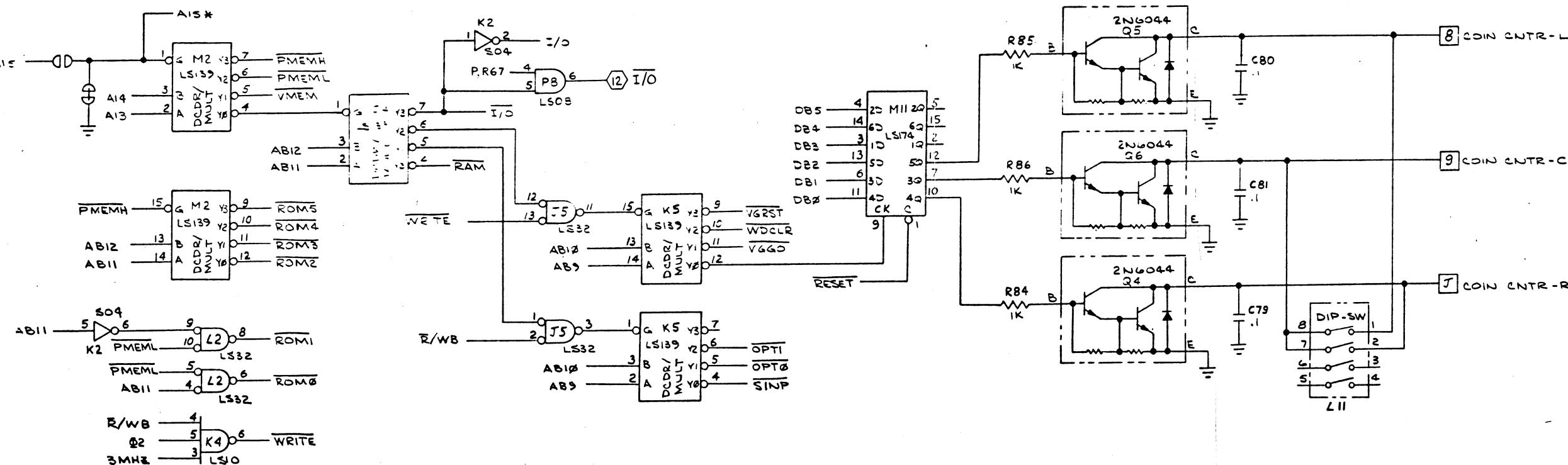
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SCHEMATIC,
6502A ANALOG VECTOR
GENERATOR P.C. BD

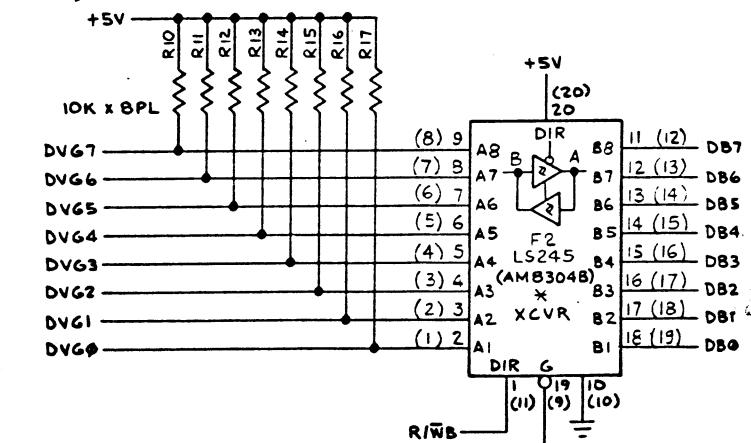
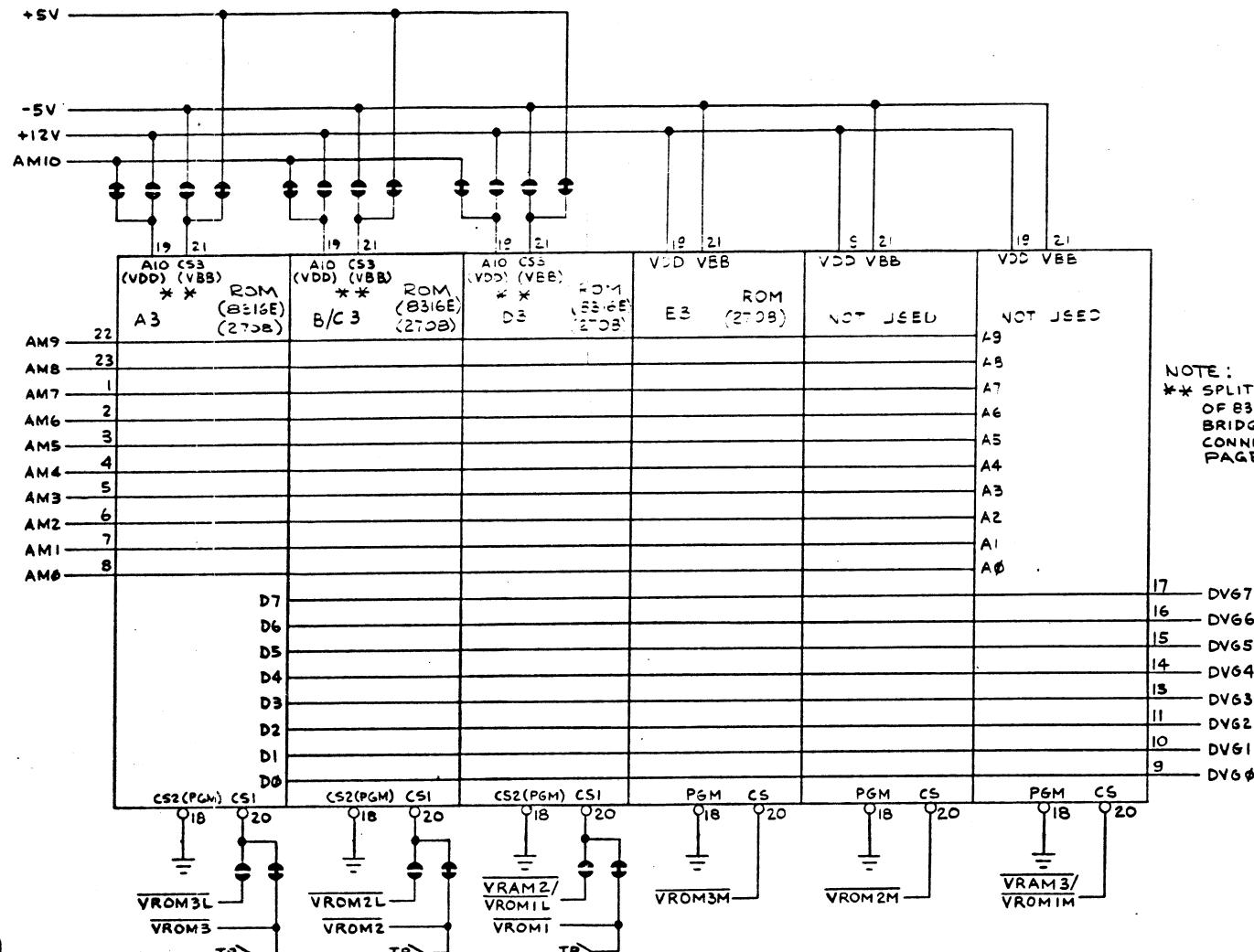
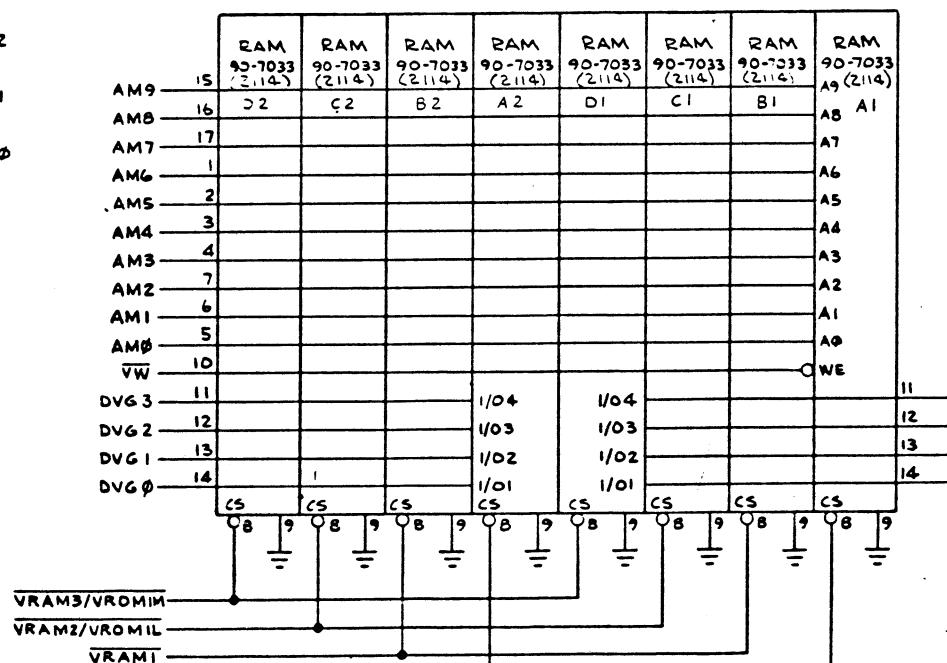
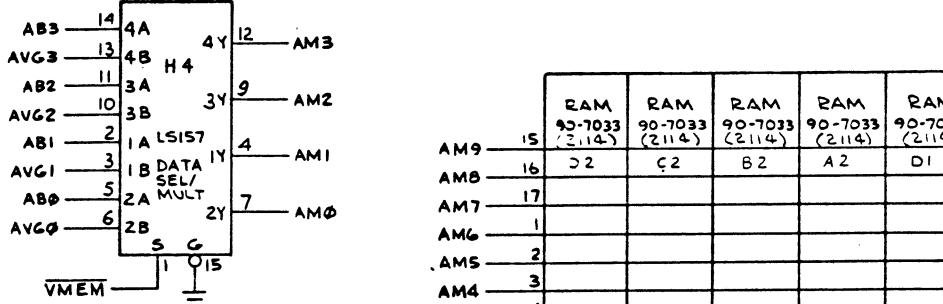
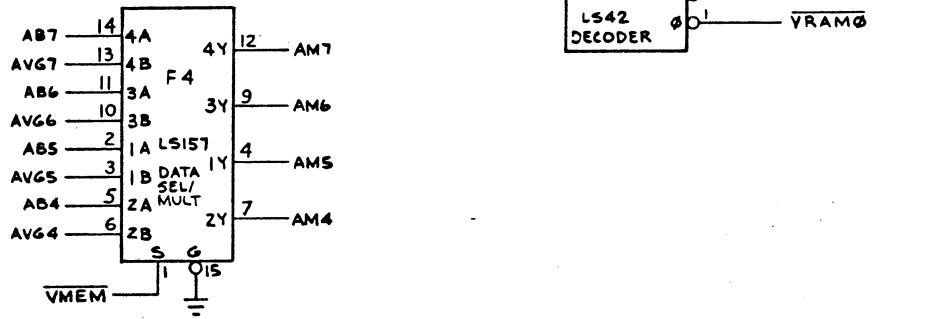
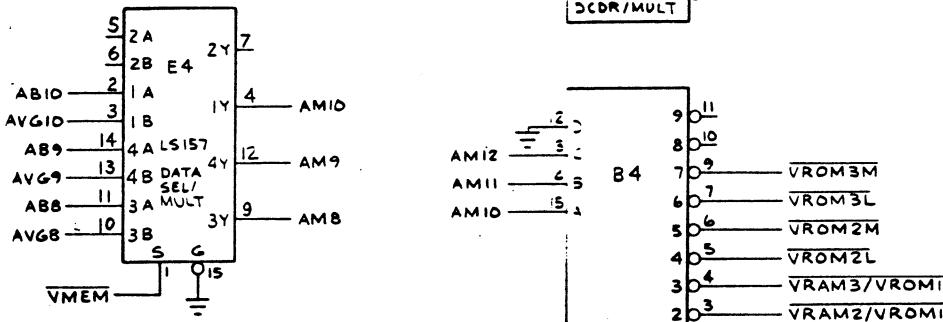
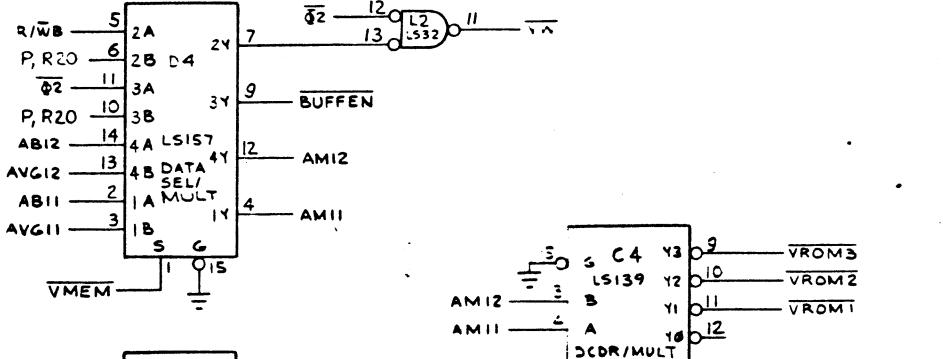
SIZE D DRAWING NO 035742-XX REV B

SCALE — SHEET 2 OF 7

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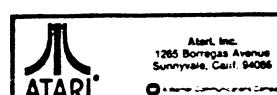


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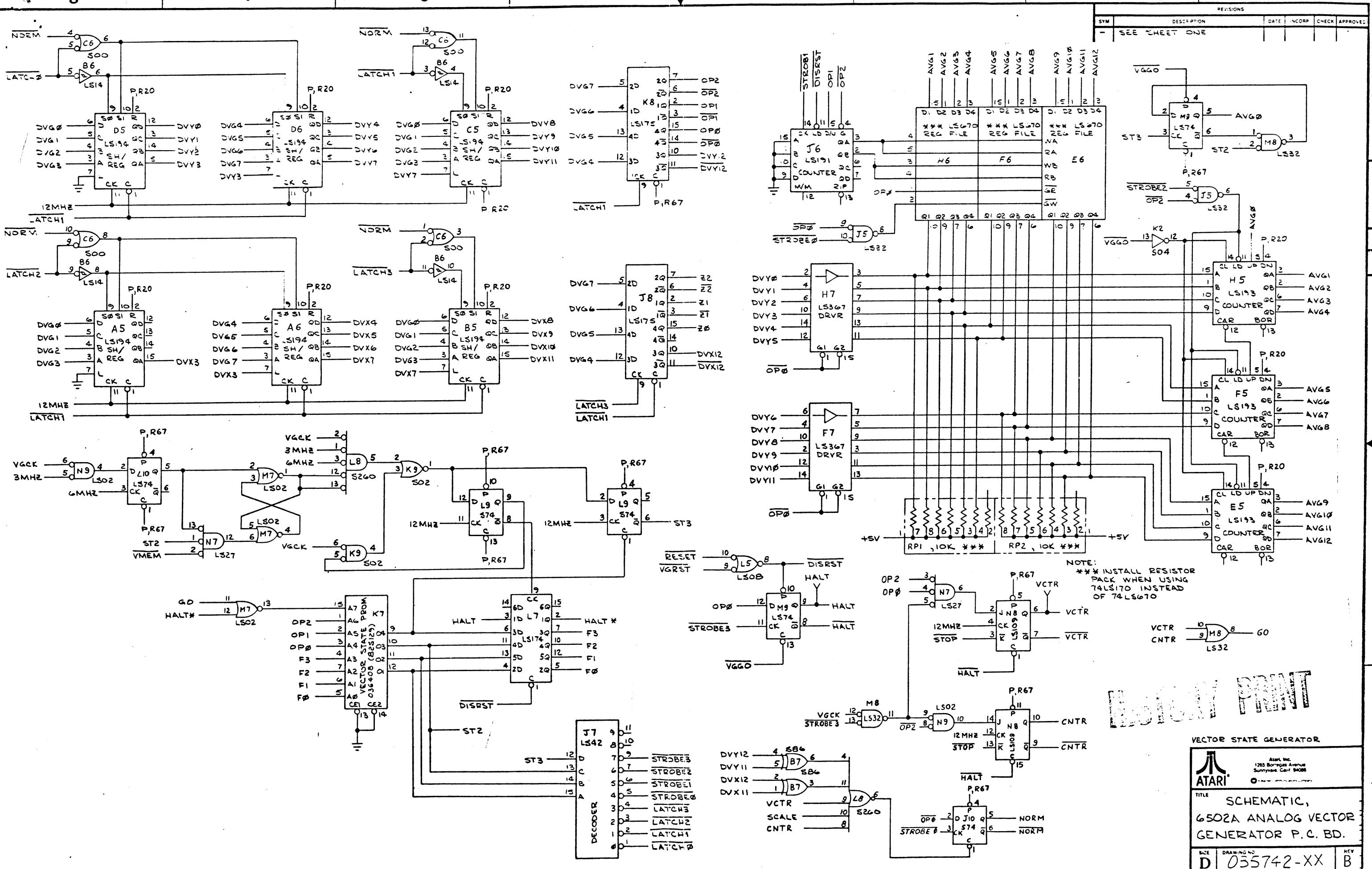
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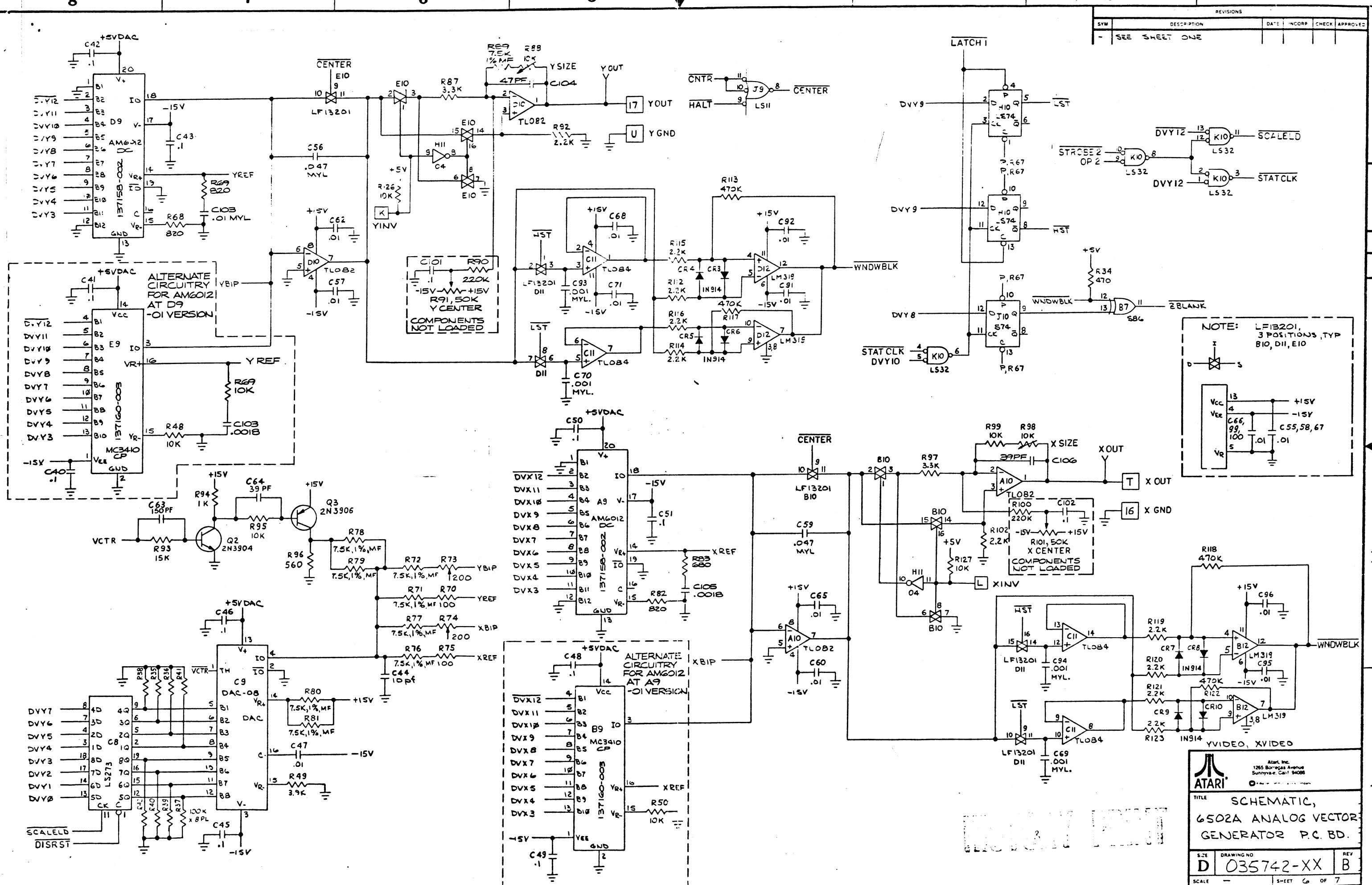
VECTOR MUX, VECTOR MEM

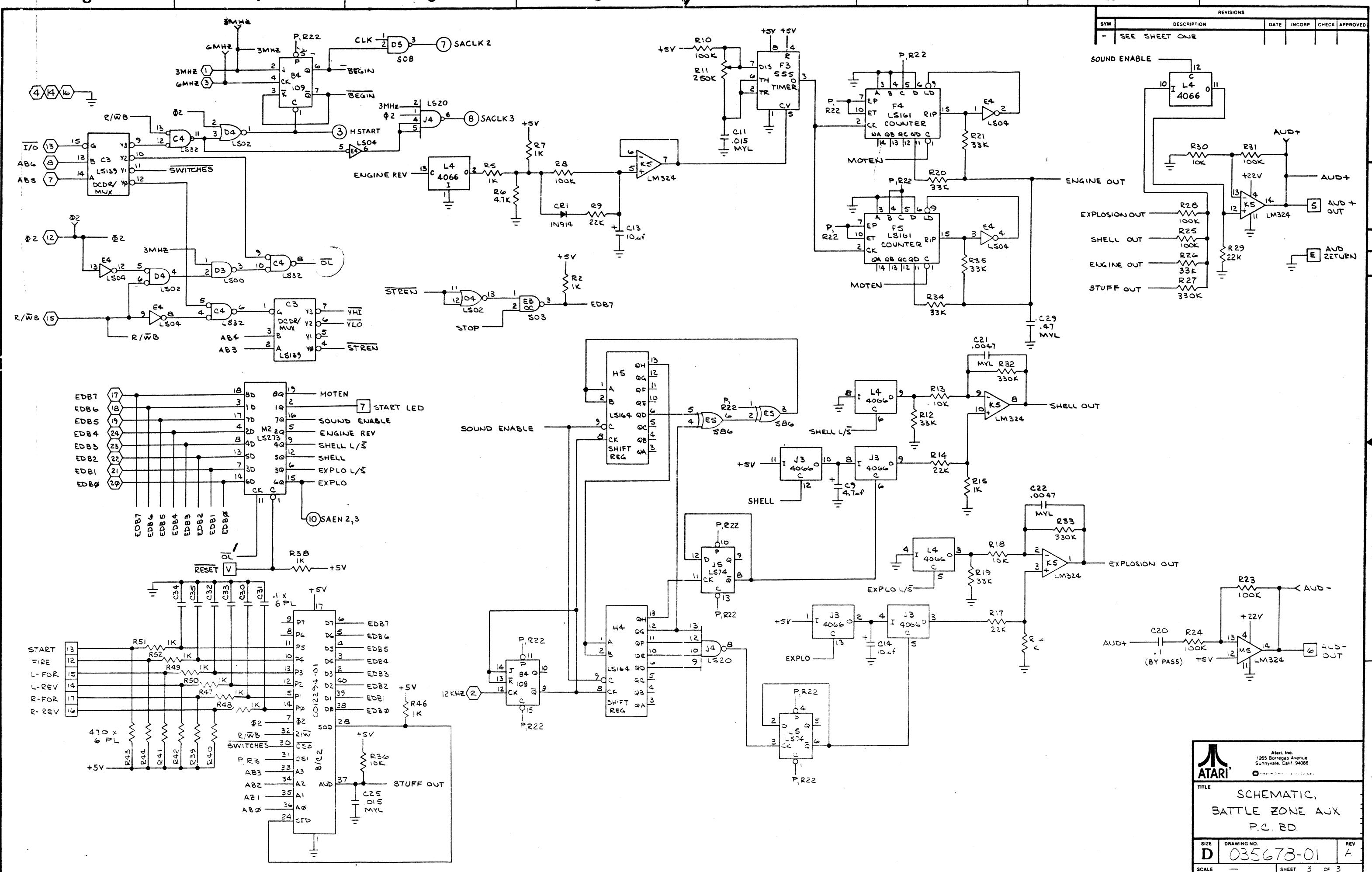


TITLE

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6502A ANALOG VECTOR
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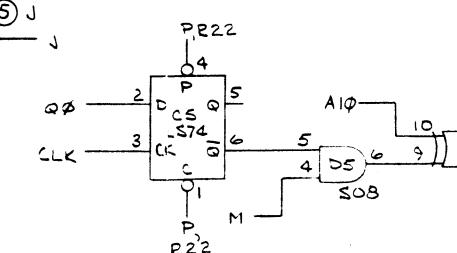
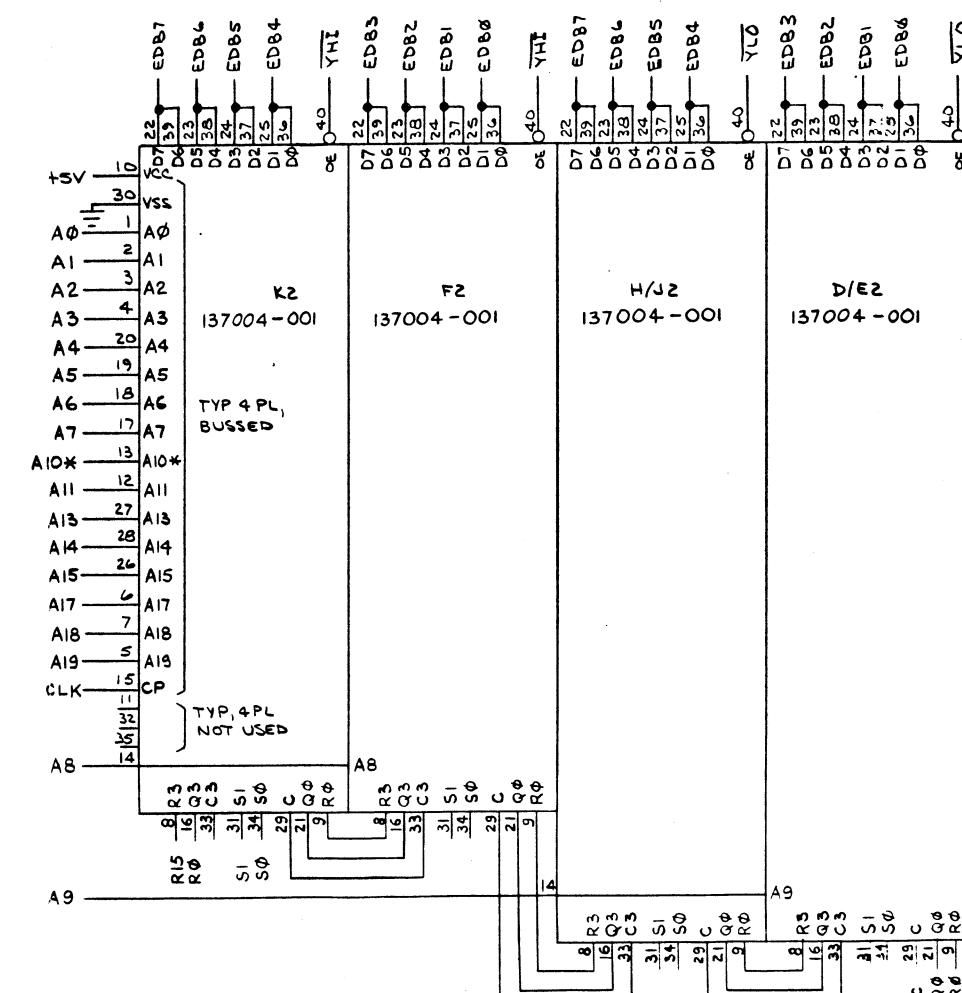
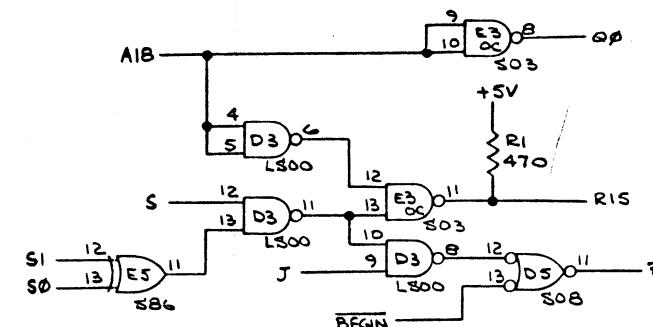
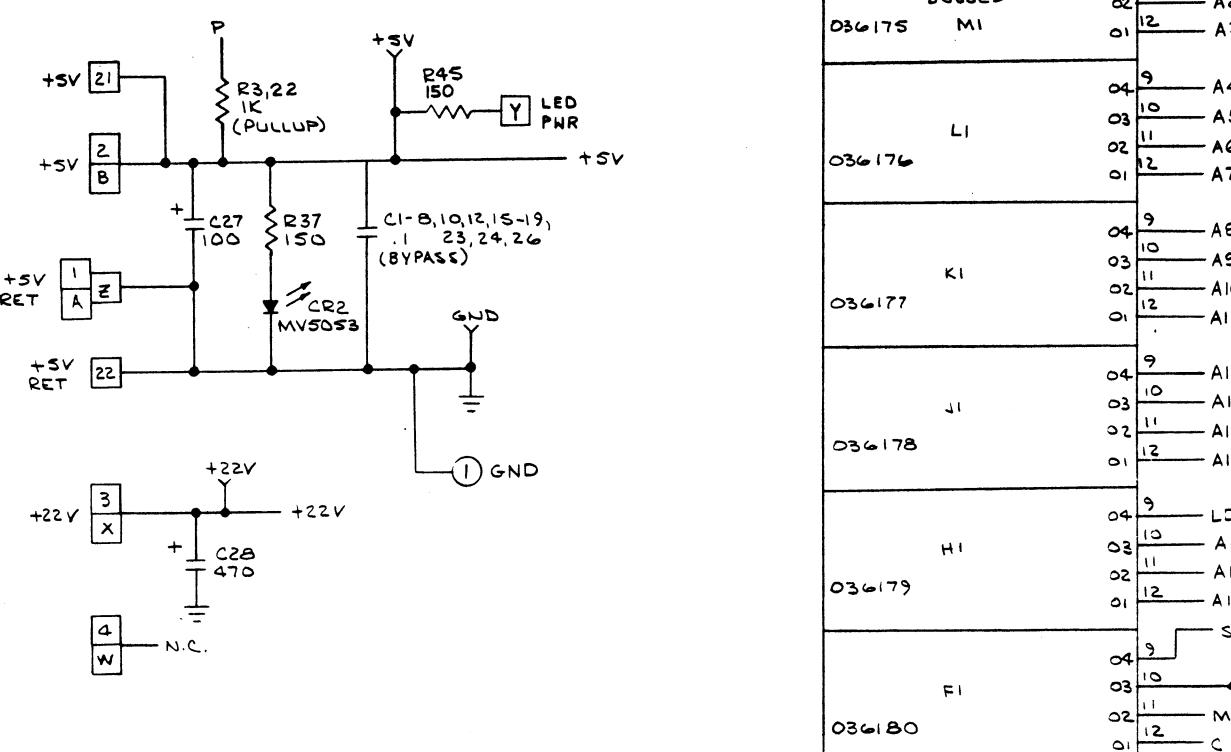
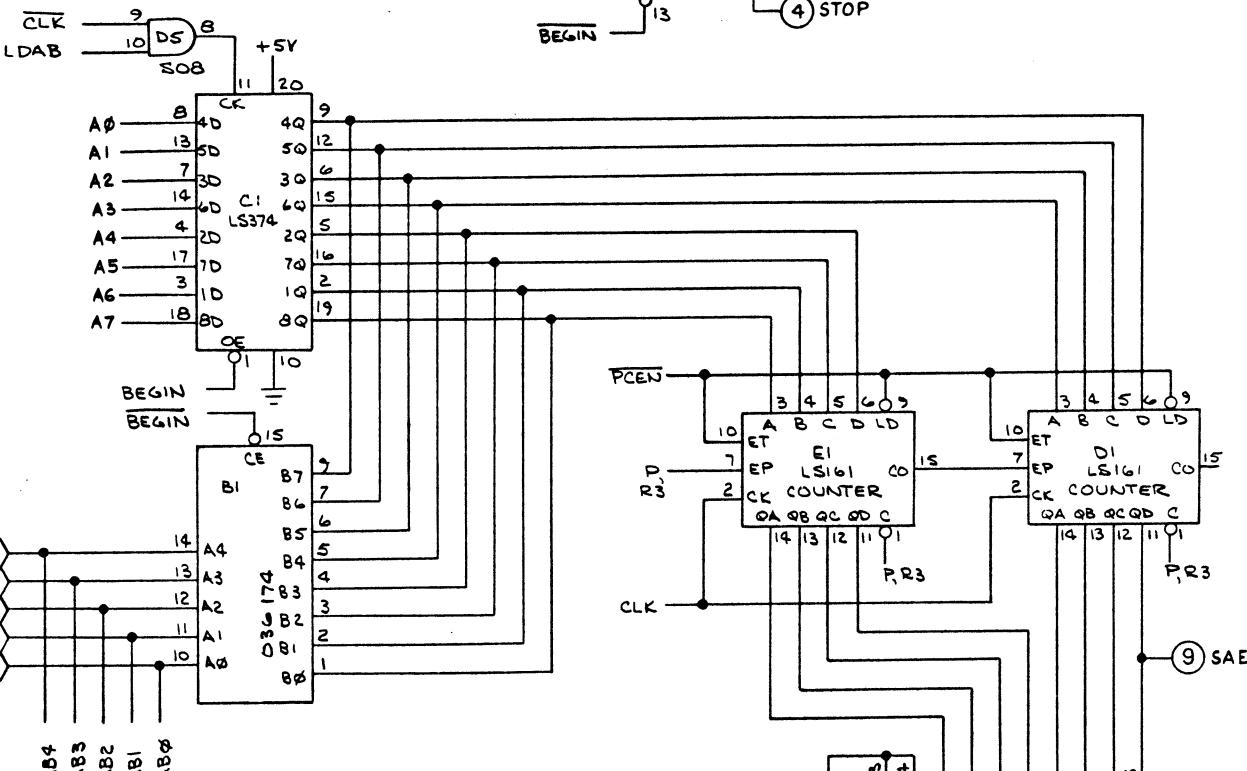
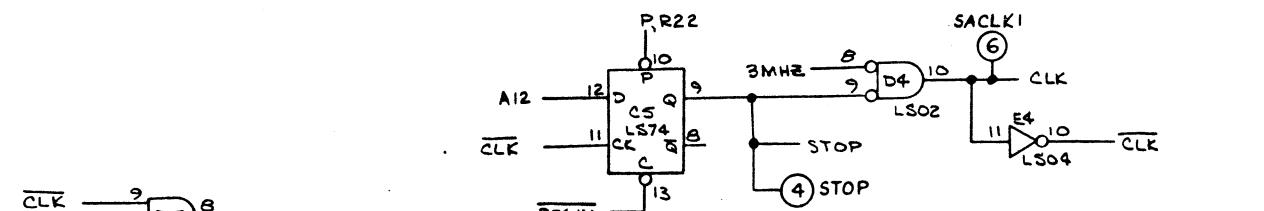






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DESCRIPTION



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		MFG ENGR	DATE	SCALE —	REV A
				SHEET 2	CF 3

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IC CHART

VOLTAGE / PIN NO.

TYPE	UNUSED GATES						
	GND	+5V	+22V	+12V	+15V	-15V	-5V
74LS00	7	14					
74LS02	7	14					
74S03	7	14					E3
74LS04	7	14					
7407	7	14					
74S08	7	14					
74LS20	7	14					
74LS32	7	14					C4
74LS74	7	14					
74S86	7	14					
74109	8	16					
74LS139	8	16					
74LS161	8	16					
74LS164	7	14					
74LS273	10	20					
74LS374	10	20					
LM324	11	4					MS, M5, M5
555	1	8					
4066	7	14					
CO1294 - 01	1	17					
137004 - 001	30	10					
825123 (PROM)	8	16					
825129 (PROM)	8	16					

MEMORY MAP

ADDRESS	DATA	FUNCTION
1XXXXXX	111111	BOARD NOT ENABLED
0111111	1111111	M BOX STATUS
1111111	1111111	M BOX READ LOW
0111111	1111111	M BOX READ HI
1111111	1111111	SWITCHES
1111111	1111111	MOTOR SOUND
1111111	1111111	START LED
1111111	1111111	SOUND ENABLE
1111111	1111111	ENGINE SOUND
1111111	1111111	STELL LOUDNESS
1111111	1111111	SHELL SOUND
1111111	1111111	EXPLOSION LOUDNESS
1111111	1111111	EXPLOSION SOUND
0111111	1111111	M BOX START

REVISIONS				
SYM	DESCRIPTION	DATE	INCORP	CHECK
3	PRE - PROD. RELEASE	6/18/80	E M	14-54
A	PROD REL.	6/18/80	E M	14-54

REFERENCE DESIG	
HIGHEST	NOT USED
R 52	
C 35	
CR 2	
	R 4

NOTES :

- 44 PIN EDGE CONNECTOR
- 24 PIN STRIP CONNECTOR
- 12 PIN TEST CONNECTOR

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ANGLES	$\pm 1^\circ$	$\pm 2^\circ$	ENGR, MECH DATE
SURFACE FINISH		± 0.005	ENGR ELECT DATE
MATERIAL:		INDUSTRIAL DESIGN DATE	
HARNESS DESIGN		PROJECT ENGR DATE	
FINISH:		MFG ENGR DATE	
SHEET 1 OF 3		DRAWING NO. 035678-01 A	

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